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Quantum capacitance and density of states of graphene

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Received 18 April 2011 Accepted for publication 8 June 2011 Published 31 January 2012 Online at stacks.iop.org/PhysScr/T146/014009

Abstract

We report capacitance measurements in top-gated graphene sheets as a function of charge carrier density. A measurement method using an *LC*-circuit provides high sensitivity to small capacitance changes and hence allows the observation of the quantum part of the capacitance. The extracted density of states has a finite value of $1 \times 10^{17} \text{ m}^{-2} \text{ eV}^{-1}$ in the vicinity of the Dirac point, which is in contrast to the theoretical prediction for ideal graphene. We attribute this discrepancy to fluctuations of the electrostatic potential with a typical amplitude of 100 meV in our device.

PACS numbers: 81.05.ue, 72.80.Vp, 84.37.+q

(Some figures may appear in colour only in the online journal)

1. Introduction

The number of available electronic states at the Fermi level determines the transport characteristics of electronic devices. A solid understanding of the density of states is hence crucial for the interpretation of the electronic properties a given system exhibits. In two-dimensional (2D) electron gases in semiconductor heterostructures, experiments mapping the density of states directly via the quantum capacitance were conducted in the 1980s [1 (and references [6–12] therein), 2] and are today standard characterization tools for these structures. More recently, the energy level spectrum of carbon nanotubes was experimentally observed in measurements of the quantum capacitance [3].

An infinite single-layer graphene sheet is expected to show a linear density of states that vanishes at the charge neutrality point [4]. The experimental findings of conductance measurements, however, contrast this theoretical prediction by showing a minimum conductivity of the order of $4(e^2/h)$, which indicates a finite charge carrier density [5, 6]. Since the quantum term in the capacitance gives insight into the density of states, both theoretical [7, 8] and experimental [9–11] studies of the quantum capacitance have been carried out for graphene within the last few years. All measurements show a nonzero value of the density of states at the Dirac point, which is commonly interpreted as originating from potential fluctuations in the graphene sheet. Here, we discuss experiments on a top-gated single-layer graphene ribbon and analyse the data in a self-consistent manner not making any theoretical assumptions about the density of states.

2. Theoretical background

A classical plate capacitor consists of two well-conducting plates arranged in parallel to each other and separated by a dielectric layer of a certain thickness. Its capacitance can be calculated considering material and geometric parameters of the system. If the density of states on one of the plates is finite, however, adding a charge carrier costs kinetic energy due to the shift of the Fermi level. Since this required extra energy reduces the total capacitance of the system, the density of states is directly reflected in an additional capacitance term. As visualized in the cross-section in figure 1(a), a locally gated graphene device can be viewed as a plate capacitor, with one plate being the top-gate electrode and the other being the graphene sheet.

The total capacitance between the gate and the electron gas can be derived from the electrostatics describing the system [12–14]. A finite applied bias $-|e|V_{\text{TG}}$ introduces a difference in the electrochemical potentials of the metal



Figure 1. (a) Schematic cross-section of a top-gated graphene sample. The graphene flake (black) is contacted with gold electrodes (orange) and covered partly by an alumina/gold top-gate (grey/red). (b) Schematic diagram of the electrostatics of the structure showing the change in electrochemical potential between the top-gate electrode and the graphene sheet. (c) Circuit scheme of the measured system.

top-gate electrode, μ_M , and of the graphene, μ_G . As sketched in figure 1(b), it consists of the Fermi energy E_F in graphene, the electrostatic potential across the dielectric as obtained by solving Poisson's equation, and the two work functions χ and W of graphene and the metal, respectively. The effective potential difference is therefore given by

$$\mu_{\rm G} - \mu_{\rm M} = |e|V_{\rm TG} = E_{\rm F} + \frac{|e|^2 n_{\rm s}}{\epsilon \epsilon_0} d + \text{const}, \qquad (1)$$

where n_s is the charge carrier density in the graphene layer, *d* is the thickness of the dielectric layer and the constant term includes the work function difference of the two materials, which can be neglected for the analysis provided that it is gate-voltage-independent. Differentiating equation (1) with respect to n_s gives an expression for the capacitance per unit area,

$$\frac{1}{C/A} = \frac{\mathrm{d}(|e|V_{\mathrm{TG}})}{e^2 \mathrm{d}n_{\mathrm{s}}} = \frac{\mathrm{d}E_{\mathrm{F}}(n_{\mathrm{s}})}{e^2 \mathrm{d}n_{\mathrm{s}}} + \frac{d}{\epsilon\epsilon_0},\tag{2}$$

where the second term on the right-hand side can be identified as the inverse of the geometric capacitance per unit area C_g/A . The other term has the dimensions of a capacitance and is inversely proportional to the density of states $\mathcal{D}(E_F) =$ dn_s/dE_F . The quantity $e^2\mathcal{D}(E_F)$ is the so-called quantum capacitance per unit area C_q/A [12], which describes an effective decrease of the total sample capacitance. Since it is connected in series to C_g , the quantum capacitance will dominate the total capacitance if it takes values smaller than C_g .

3. Measurement setup

The temperature for all measurements was 1.7 K, achieved in a variable temperature insert ⁴He cryostat. For basic characterization of the device, transport measurements using standard lock-in techniques were carried out in addition to quantum capacitance measurements. For the latter, the size of the expected signal was of the order of several fF and hence a high measurement resolution was required. Since frequencies can be measured with high accuracy, we used an *LC*-circuit and recorded changes in its resonance frequency $f_{\rm res}$. Figure 1(c) shows the circuit model of our setup, with the oscillator consisting of an inductor ($L = 100 \,\mu$ H) placed at room temperature in parallel to the setup capacitance *C*. The main contribution to *C* comes from the wiring of the cryostat with a capacitance $C_{\rm cables} \approx 340 \,\text{pF}$ for the coaxial cables. In parallel to this, the series connection of $C_{\rm g}$ and $C_{\rm q}$ forms the sample capacitance $C_{\rm s}$.

During the measurements, an oscillation amplitude of 20 mV was maintained in the resonator by an external drive. The resulting self-resonant frequencies were 850–900 kHz and the relative sensitivity for frequency changes was $\Delta f \approx 2.5 \times 10^{-7}$. Converted to capacitance changes, an accuracy of the order of 170 aF was achievable with this setup.

4. Sample fabrication

Standard mechanical exfoliation [15] of graphite flakes was used to obtain few-layer graphene on Si/SiO₂ substrate. To exclude stray capacitances from the back gate, the silicon substrate was undoped. Single-layer flakes were identified using an optical microscope, and subsequent atomic force microscopy (AFM) measurements as well as Raman spectroscopy [16, 17] were carried out to verify their single-layer nature. By electron beam lithography and subsequent evaporation of Cr/Au (2nm/40nm), electrical contacts were defined. The graphene sheets were structured by reactive ion etching and a patterned top-gate was deposited after a third electron beam lithography step. As a dielectric for the top-gate, we used Al₂O₃, which was obtained in cycles of depositing 1 nm of aluminum followed by a 3 min period of increased oxygen pressure in the deposition chamber leading to the complete oxidation of the thin Al film. The resulting oxide thickness of four such cycles is approximately 12 nm. Deposition of Ti/Au (5 nm/50 nm) in the same evaporation chamber formed the top-gate electrode. Figure 2(a) displays an AFM image of the device measured for this work.

5. Results and discussion

Figure 2(b) shows a two-terminal transport measurement taken with a current bias of 1 nA. The Dirac point is visible as a resistance maximum and only slightly shifted away from zero top-gate voltage. Since we do not know the precise thickness and the dielectric constant of the aluminum oxide, we can only estimate the geometric capacitance of the top-gate to be $C_g \approx 6 \, \text{fF} \, \mu \text{m}^{-2}$. Using this estimate, a mobility of 2000–3000 cm² V s⁻¹ is extracted for the device.

The change in resonance frequency $f_{\rm res}$ as a function of applied top-gate voltage $V_{\rm TG}$ is displayed in figure 3(a). A maximum is observed close to zero gate voltage, which coincides with the maximum in the transport curve (figure 2(b)) and therefore marks the Dirac point. At high charge carrier densities, the resonance frequency reaches a constant value, indicating the dominant influence of the cable capacitance. We are interested in the relative change of $f_{\rm res}$



Figure 2. (a) Atomic force microscope image of the device studied here. The electrodes are coloured corresponding to the scheme in figure 1(a). (b) Two-point resistance obtained in transport measurements.

and therefore define the magnitude $\delta_m \equiv \Delta f(V_{TG})/f_0$, with f_0 being the voltage-independent background frequency (see the right scale in figure 3(a)).

To determine the changes in quantum capacitance from the frequency measurement, the circuit diagram has to be considered. Far away from the Dirac point, we expect C_q to be too large to affect the total capacitance, which is hence given by $C_{\text{cables}} + C_g$ in this limit. The total capacitance of the equivalent circuit in figure 1(d) can be written as

$$C(V_{\rm TG}) = C_{\rm cables} + C_{\rm g} - \left[C_{\rm g} - \left(\frac{1}{C_{\rm g}} + \frac{1}{C_{\rm q}}\right)^{-1}\right],$$
 (3)

where the term outside the square brackets is constant at all top-gate voltages and only the term inside is varying, causing the change in capacitance $\Delta C(V_{\text{TG}})$. This expression describing ΔC can be simplified to $C_g^2/(C_g + C_q)$.

The resonance condition of an *LC*-circuit $f_{\rm res} = 1/(2\pi\sqrt{LC(V_{\rm TG})})$ can be expanded for small variations ΔC of the capacitance, yielding $\Delta C = -2(C_{\rm cables} + C_{\rm g})\Delta f/f_0$. Applying this relation to the frequency data yields the curve displayed in figure 3(b). A minimum is visible at the charge neutrality point and an increase of ΔC with increasing density is observed. In this regime, the quantum capacitance value at large top-gate voltages indicates that $C_{\rm q}$ is negligible beyond ± 1 V.

Combining the two expressions for ΔC relates the quantum capacitance to the cable capacitance, the geometric capacitance and the measured frequency:

$$C_{\rm q} = C_{\rm g} \frac{\alpha - \delta_{\rm m}}{\delta_{\rm m}},\tag{4}$$



Figure 3. (a) Resonance frequency as a function of applied top-gate voltage V_{TG} . (b) Change in total capacitance determined from the frequency measurements in (a). (c) Relation between Fermi energy and top-gate voltage as deduced from equation (7). The different curves (black lines) are obtained for different values of C_g (from the outermost to the innermost curve: 4.8, 5.8 and 6.8 fF μ m⁻²). The dashed curve shows the theoretically expected dependence for a perfectly clean graphene sheet. To compensate for the shift of the Dirac point towards negative voltages, the horizontal axis in (a), (b) and (c) is offset by 0.3 V.

where $\alpha = C_g/2C_{\text{cables}}$. This equation contains only experimental parameters and will later be used to deduce the density of states $\mathcal{D}(E) = C_q(V_{\text{TG}})/|e|^2$.

In order to extract the density of states as a function of the Fermi energy, the top-gate voltage axis has to be transformed into energy. To avoid any *a priori* assumptions about the density of states, we do not use the linear dispersion but start with the electrostatic configuration described by equation (1). The charge carrier density n_s is obtained by integrating the density of states $\mathcal{D}(E)$ over all energies between the Dirac point and the Fermi energy E_F and can be substituted to give

$$|e|V_{\rm TG} = E_{\rm F} + \frac{|e|^2}{\epsilon\epsilon_0} d \int_0^{E_{\rm F}} \mathcal{D}(E) dE.$$
 (5)

As shown above, both the geometric capacitance C_g and the quantum capacitance C_q enter this expression. The change in Fermi energy for a given change in top-gate voltage can hence

be written as

$$\frac{\partial E_{\rm F}}{\partial V_{\rm TG}} = |e| \left[1 + \frac{C_{\rm q}(V_{\rm TG})}{C_{\rm g}} \right]^{-1}.$$
 (6)

The relation between the capacitances involved and the measured frequency identified before can be included in equation (6). An expression for the Fermi energy which depends on the applied top-gate voltage is gained by integration over V_{TG} :

$$E_{\rm F}(V_{\rm TG}) = \frac{|e|}{\alpha} \int_{V_{\rm D}}^{V_{\rm TG}} \delta_{\rm m}(V_{\rm TG}') \mathrm{d}V_{\rm TG}', \tag{7}$$

where $V_{\rm D}$ is the gate voltage at the Dirac point. Here the geometric capacitance is the only parameter our device does not allow us to determine from the data directly. We can however estimate it from the parallel plate capacitor geometry (as has been done above to deduce the charge carrier mobility) to be $C_{\rm g} \approx 6 \, {\rm fF} \, \mu {\rm m}^{-2}$. In addition to the ribbon device, a bilayer Hall bar located on the same chip was used to determine the charge carrier density from Hall measurements. A geometric capacitance of $C_{\rm g} \approx 5.8 \, {\rm fF} \, \mu {\rm m}^{-2}$ was obtained by this method. Since the two values are comparable, the estimate for the free parameter $C_{\rm g}$ seems reasonable.

Equation (7) can now be applied to the data assuming a specific C_g . In figure 3(c), the obtained Fermi energy is plotted as a function of top-gate voltage for three different values of C_g as well as for an ideal graphene sheet (dashed curve). As expected, an increase of $|E_{\rm F}|$ with increasing $|V_{\text{TG}}|$ is observed. This effect is strong at small voltages. The (unphysical) saturation at $|V_{TG}| > 1$ V indicated in figure 3 is due to the dominating cable capacitance. For all experimental curves, the Fermi energy at a certain top-gate voltage is below the theoretically expected value. As the noise in the frequency measurement leads to an error in the $E_{\rm F}(V_{\rm TG})$ relationship extracted from the measurement, an error analysis is done. The accuracy is estimated assuming a Gaussian probability density distribution for the relative frequency change $\Delta f/f_0$. Its width is given by the variance of the noise in δ_m , yielding $\sigma = 2.5 \times 10^{-7}$. The resulting error bars indicated in figure 3(c) are small in comparison to the uncertainty given by the parameter $C_{\rm g}$.

Equations (4) and (7) now allow us to convert the data in figure 3(b) into a $\mathcal{D}(E_{\rm F})$ plot. The result is shown in figure 4 for different $C_{\rm g}$ values between 4.8 and 6.8 fF μ m⁻². For comparison, the dashed line in figure 4 displays the theoretical density of states of perfectly clean graphene given by $\mathcal{D}(E_{\rm F}) = 2E_{\rm F}/\pi (v_{\rm F}\hbar)^2$ [4]. In particular, at low energies a large discrepancy is observed between experiment and theory. Instead of the linear increase starting at zero, a nearly constant value of $\mathcal{D}(E_{\rm F}) \approx 1 \times 10^{17} \, {\rm m}^{-2} \, {\rm eV}^{-1}$ is maintained in the interval between $E_{\rm F} = \pm 50$ meV. An increase in the number of states towards higher energies is observed. However, since the signal to noise ratio decreases rapidly as the measurement value is constant at high charge carrier densities, the errors in the extracted $\mathcal{D}(E_{\rm F})$ become very large outside the interval limited by the grey areas in figure 4. The full width at half maximum of the probability density function of $\mathcal{D}(E_{\rm F})$ was used to determine the error bars shown in the graph.

The observed nonzero density of states around the charge neutrality point indicates a finite number of states. This is



Figure 4. Density of states as a function of Fermi energy. The black solid lines show the experimental data assuming different C_g (from the outermost to the innermost curve: 4.8, 5.8 and 6.8 fF μ m⁻²). Error bars are indicated and the theoretically expected density of states for a perfectly clean graphene sheet is drawn as the dashed line. The bias window defined in figure 3 is plotted with dotted lines and the disorder amplitude is indicated by the red arrow.

conceivable in the presence of local potential fluctuations in the graphene sheet. From the flat portion of the density of states in figure 4, we can estimate a characteristic amplitude of the fluctuations of 100 meV. Comparing our results with transport measurements on graphene nanoribbons [18–22] and scanning tunnelling electron transistor experiments [23], we find good agreement. As these samples were not covered with any dielectric unlike the sample used in our experiment, we can infer that the gate oxide did not induce a large amount of additional disorder in our graphene device.

6. Conclusions

We have performed transport and capacitance measurements on a locally gated single-layer graphene sheet. With our measurement setup, using a resonant circuit we could measure the capacitance with very high sensitivity. The density of states as a function of Fermi energy was determined from experimental data. As the main results, we extracted a constant density of states $\mathcal{D}(E_{\rm F}) \approx 1 \times 10^{17} \, {\rm m}^{-2} \, {\rm eV}^{-1}$ around the Dirac point and determined the size of the disorder potential to be $\approx 100 \, {\rm meV}$, which is comparable to other studies.

It has recently been shown that the reduction of the disorder fluctuations (e.g. by using different substrates [24, 25]) improves the quantum capacitance signal dramatically and even allows for the investigation of Landau level formation in capacitance measurements [26–28].

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